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CNF			<p><u>A 35 ns-cycle-time 3.3 V-only 32 Mb NAND flash EEPROM</u> Imamiya, K.; Iwata, Y.; Sugiura, Y.; Nakamura, H.; Oodaira, H.; Momodomi, M.; Ito, Y.; Watanabe, T.; Araki, H.; Narita, K.; Masuda, K.; Miyamoto, J. Solid-State Circuits Conference, 1995. Digest of Technical Papers. 41st ISSCC, 1995 IEEE International , 1995 ,</p>

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A 3.3 V-only 16 Mb DINOR flash memory

Kobayashi, S.; Mihara, M.; Miyawaki, Y.; Ishii, M.; Futatsuya, T.; Hosogane, A.; Ohba, A.; Terada, Y.; Ajika, N.; Kunori, Y.; Yuzuriha, K.; Hatanaka, M.; Miyoshi, H.; Yoshihara, T.; Uji, Y.; Matsuo, A.; Taniguchi, Y.; Kiguchi, Y.

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New submicron non-volatile memory modules for 16/32-bit devices

Kuo, C.; Morton, B.; Toms, T.; Weidner, M.; Chrudimsky, D.; Choe, H.; Bowers, M.; Yeon-seuk Kim; Chang, K.M.; Smith, P.

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A 1 Mbit EEPROM with MONOS memory cell for semiconductor disk application

Nozaki, T.; Tanaka, T.; Kijiya, Y.; Kinoshita, E.; Tsuchiya, T.; Hayashi, Y.

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A 35 ns cycle time & 3 V only 32 Mb NAND flash EEPROM

Iwata, Y.; Imamiya, K.; Sugiura, Y.; Nakamura, H.; Oodaira, H.; Momodomi, M.; Itoh, Y.; Watanabe, T.; Araki, H.; Narita, K.; Masuda, K.; Miyamoto, J.-I.

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A 60-ns 16-Mb flash EEPROM with program and erase sequence controller

Nakayama, T.; Kobayashi, S.; Miyawaki, Y.; Terada, Y.; Ajika, N.; Ohi, M.; Arima, H.; Matsukawa, T.; Yoshihara, T.; Suzuki, K.

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